

**IN THE SPECIFICATION**

Please replace the paragraph bridging pages 5 and 6 with the following:

[0019] If the command is a write command, for transmitting data from the initiator 110 to the target 120, the data that is to be transmitted is presented at the interface 115 at the same time that the write command-request signal is asserted by the interface 115. The target 120 then accepts the data, which is present at the interface 126 when the corresponding command-select control signal is received at the interface 125, and asserts the command-complete control signal via the interface 125. Upon receipt of the corresponding command-done control signal at the interface 115, the initiator 110 is free to release the bus by deasserting the command-request control signal, and need no longer maintain the address and data signals at the interface 125.